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1 Design tradeoffs for the Alpha EV8 conditional branch predictor

André Semer, Stephen Felix, Venkata Krishnan, Jyannakis Saxeides

May 2002

ISCA '02: Proceedings of the 29th annual international symposium on Computer architecture

Publisher: ACM

Full text available: PDF (1.24 MB)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 126, Downloads (Overall): 783, Citation Count: 41

This paper presents the Alpha EV8 conditional branch predictor. The Alpha EV8 microprocessor project, canceled June 2001 in a late phase of development, envisioned an aggressive 8-wide issue out-of-order superscalar microarchitecture featuring a very ...

Keywords: EV8 processor, Branch Prediction

Also published in:

May 2002 SIGARCH Computer Architecture News Volume 30 Issue 2

2 Propositional logics of programs: systems, models, and complexity

Robt Barak

January 1980 POPL '80: Proceedings of the 7th ACM SIGPLAN-SIGACT symposium on Principles of programming languages

Publisher: ACM Request Permissions

Full text available: PDF (585.57 KB)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 7, Downloads (Overall): 129, Citation Count: 2

We indicate below the various results in this paper and the sections where these results are fully described. 51 is introductory.(52). The partial correctness assertion (PCA) $A\{e\}B$ is expressed in PDL in the form $A \rightarrow [e]B$

3 Supporting RFID-based item tracking applications in Oracle DBMS using a bitmap datatype

Ying Lu, Soema Sundara, Timothy Choma, Jaganathan Srinivasan

August 2005

Vldb '05: Proceedings of the 31st international conference on Very large data bases

Publisher: VLDB Endowment

Full text available: PDF (200.93 KB)

Bibliometrics: Downloads (6 Weeks): 221, Downloads (12 Months): 2581, Downloads (Overall): 4725, Citation Count: 9

Radio Frequency Identification (RFID) based item-level tracking holds the promise of revolutionizing supply-chain retail store, and asset management applications. However, the high volume of data generated by item-level tracking poses challenges to ...

4 TrACS: transceiver architecture and wireless channel simulator

Chitturase Ramesh, Aya Raza, Mohamed Ismail, Mikael Skoglund

September 2007

SBCCI '07: Proceedings of the 20th annual conference on Integrated circuits and system design

Publisher: ACM

Full text available: PDF (981.79 KB)

Bibliometrics: Downloads (6 Weeks): 135, Downloads (12 Months): 212, Downloads (Overall): 326, Citation Count: 1

This paper presents the design of a system-level simulator for radio receivers, including receiver circuits, in Matlab. The system level outlook offers a better characterization of circuit design, as the signal processing in the digital receiver is asymmetric ...

Keywords: performance verification, receiver, simulator, system level design, transceiver circuits, wireless sensor node design

5 [Applying natural language technology to the learning of operating systems functions](#)
B. Lees, J. Cowie

January 1996 **ITICSE '96**: Proceedings of the 1st conference on Integrating technology into computer science education

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (346.72 KB)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 22, Downloads (Overall): 145, Citation Count: 0

Also published in:

January 1996 **SIGCUE Outlook** Volume 24 Issue 1-3

June 1996 **SIGCSE Bulletin** Volume 28 Issue SI

6 [Webtrees](#)

Michael Tupper

September 1999

netWorker, Volume 3 Issue 3

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (8.73 KB), [PDF](#) (32.36 KB)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 1, Downloads (Overall): 49, Citation Count: 0

7 [Scalable sequential pattern mining for biological sequences](#)

Ke Wang, Yabo Xu, Jinteng Xu, Yu

November 2004 **CIKM '04**: Proceedings of the thirteenth ACM international conference on Information and knowledge management

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (237.01 KB)

Bibliometrics: Downloads (6 Weeks): 77, Downloads (12 Months): 485, Downloads (Overall): 1326, Citation Count: 0

Biosequences typically have a small alphabet, a long length, and patterns containing gaps (i.e., "don't care") of arbitrary size. Mining frequent patterns in such sequences faces a different type of explosion than in transaction sequences primarily motivated ...

Keywords: algorithm, bioinformatics, frequent pattern, pruning technique, sequence, sequential pattern

8 [Correlation between heart rate, electrodermal activity and player experience in first-person shooter games](#)

Anders Drachmann, Lennart E. Nacke, Georgios Yannakakis, Anja Lee Pedersen

July 2010 **Sandbox '10**: Proceedings of the 5th ACM SIGGRAPH Symposium on Video Games

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (2.17 MB)

Bibliometrics: Downloads (6 Weeks): 20, Downloads (12 Months): 107, Downloads (Overall): 107, Citation Count: 0

Psychophysiological methods are becoming more popular in game research as covert and reliable measures of affective player experience, emotions, and cognition. Since player experience is not well understood, correlation between self-reports from players ...

Keywords: affective gaming, analysis, digital games, entertainment, human-centered design, player experience, psychophysiology, user experience (UX), user studies

9 [Teaching wireless sensor networks through testbed development](#)

Anna Forster, Mahdi Jazayeri

November 2009 **SenSys '09**: Proceedings of the 7th ACM Conference on Embedded Networked Sensor Systems

Publisher: ACM

Full text available: [PDF](#) (562.96 KB)

Bibliometrics: Downloads (6 Weeks): 72, Downloads (12 Months): 149, Downloads (Overall): 193, Citation Count: 0

The rapid development of wireless sensor networks (WSNs) and the increasing complexity of the deployed applications force researchers to turn to testbeds to test their communication and application related algorithms protocols. On the other side, ...

Keywords: MSB430, design, education, testbed, wireless sensor networks

- 10 [On the design of the local variable cache in a hardware translation-based java virtual machine](#)
[Intouka Gu](#)



July 2005 **LCTES '05**: Proceedings of the 2005 ACM SIGPLAN/SIGBED conference on Languages, compilers, and tools for embedded systems

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (118.36 KB)

Bibliometrics: Downloads (6 Weeks): 25, Downloads (12 Months): 274, Downloads (Overall): 750, Citation Count: 3

Hardware bytecode translation is a technique to improve the performance of the Java Virtual Machine (JVM), especially on the portable devices for which dynamic compilation is infeasible. However, since the translation is on a single bytecode basis, ...

Keywords: hardware-translation, java virtual machine, memory hierarchy

Also published in:

July 2005 **SIGPLAN Notices** Volume 40 Issue 7

- 11 [Planning collision-free reaching motions for interactive object manipulation and grasping](#)
[Marcelo Kalman, Amiruv Aubel, Tolga Abaci, Daniel Thalmann](#)



August 2008 **SIGGRAPH '08**: SIGGRAPH 2008 classes

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (870.05 KB)

Bibliometrics: Downloads (6 Weeks): 37, Downloads (12 Months): 108, Downloads (Overall): 344, Citation Count: 1

We present new techniques that use motion planning algorithms based on probabilistic roadmaps to control 22 degrees of freedom (DOFs) of human-like characters in interactive applications. Our main purpose is the automata synthesis of collision-free ...

- 12 [Adaptive Allocation of Central Processing Unit Quanta](#)



[D. Patkar, E. Gelerker, J. Lentini](#)

January 1978 **Journal of the ACM (JACM)**, Volume 23 Issue 1

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (332.88 KB)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 6, Downloads (Overall): 214, Citation Count: 4

A method is presented for numerically inverting a Laplace transform that requires, in addition to the transform function itself, only sine, cosine, and exponential functions. The method is conceptually much like the method of Dubner and Abate, which ...

- 13 [Investigation of mutation operators for the bayesian optimization algorithm](#)



[Eric M. Jensen, Tomoyuki Hirovayu, Noriyuki Fujimoto](#)

July 2007 **GECCO '07**: Proceedings of the 9th annual conference on Genetic and evolutionary computation

Publisher: ACM

Full text available: [PDF](#) (98.98 KB)

Bibliometrics: Downloads (6 Weeks): 21, Downloads (12 Months): 44, Downloads (Overall): 118, Citation Count: 0

Although BOA is effective at finding solutions for optimization problems, small population sizes in a model can re in premature convergence to a sub-optimal solution. One way of avoiding premature convergence is to increase population diversity with ...

Keywords: Bayesian optimization algorithm, mutation, probabilistic model building genetic algorithm

- 14 [Semantic optimization: what are disjunctive residues useful for?](#)



[Wafariz J. A. Kowaczuk](#)

September 1992 **SIGMOD Record**, Volume 21 Issue 3

Publisher: ACM

Full text available: [PDF](#) (476.57 KB)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 10, Downloads (Overall): 94, Citation Count: 0

Residues have been proved to be a very important means for doing semantic optimization. In this paper we will discuss a new kind of residues—the disjunctive residues. It will be shown that they are very useful to perform subformula elimination, ...

- 15 [A queueing-theoretic foundation of available bandwidth estimation: single-hop analysis](#)




[Xiang Liu, Kishore Ravindran, Dattin Lenguyen](#)

August 2007

IEEE/ ACM Transactions on Networking (TON) , Volume 15 Issue 4

Publisher: IEEE Press

Full text available:  (849.67 KB)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 79, Downloads (Overall): 310, Citation Count: 5

Most existing available-bandwidth measurement techniques are justified using a constant-rate fluid cross-traffic model. To achieve a better understanding of the performance of current bandwidth measurement techniques in general traffic conditions, this ...

Keywords: active measurement, bandwidth estimation, packet-pair sampling

16 [Searching and browsing text collections with large category hierarchies](#)

 March A. Hearst, Chandu Karkut

March 1997

CHI '97: CHI '97 extended abstracts on Human factors in computing systems: looking to the f.


Publisher: ACM Full text available:  (272.42 KB)

Bibliometrics: Downloads (6 Weeks): 22, Downloads (12 Months): 44, Downloads (Overall): 129, Citation Count: 4

A new user interface has been developed that allows users to make use of large category hierarchies for search . browsing of retrieval results for information access. The key insight is the separation of the representation of category labels from documents, ...

Keywords: categories, information access, information visualization, search, text

17 [NER systems that suit user's preferences: adjusting the recall-precision trade-off for entity extraction](#)

 Anat Minkov, Richard C. Wang, Anthony Tomasic, William W. Cohen

June 2006

NAACL-Short '06: Proceedings of the Human Language Technology Conference of the NAACL, Companion Volume: Short Papers

Publisher: Association for Computational Linguistics

Full text available:  (73.67 KB)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 29, Downloads (Overall): 38, Citation Count: 1

We describe a method based on "tweaking" an existing learned sequential classifier to change the recall-precision tradeoff, guided by a user-provided performance criterion. This method is evaluated on the task of recognizing personal names in email and ...

18 [Reducing fine-grain communication overhead in multithread code generation for heterogeneous MPSoC](#)

 Lisane Brisolara, Sang-Ho Han, Xavier Guerci, Luis Carru, Ricardo Reis, Sae-Il Chae, Ahmed Jeraya

April 2007

SCOPES '07: Proceedings of the 10th International Workshop on Software & compilers for embedded systems


Publisher: ACM

Full text available:  (643.34 KB)

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 43, Downloads (Overall): 286, Citation Count: 3

Heterogeneous MPSoCs present unique opportunities for emerging embedded applications, which require both hi performance and programmability. Although, software programming for these MPSoC architectures requires tedious and error-prone tasks, thereby ...

19 [Integrated support environments for simulation modelling](#)

 David W. Balmier, Ray J. Paas

December 1990

WSC '90: Proceedings of the 22nd conference on Winter simulation

Publisher: IEEE Press

Full text available:  (873.15 KB)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 15, Downloads (Overall): 220, Citation Count: 5

20 [Joint pricing-network design and stochastic traffic engineering to manage demand uncertainty](#)

 Debasis Mishra

June 2005

SIGMETRICS '05: Proceedings of the 2005 ACM SIGMETRICS international conference on Measurement and modeling of computer systems

Publisher: ACM Full text available:  (101.93 KB)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 200, Downloads (Overall): 535, Citation Count: 0

I will describe two networking models, together with their optimization techniques, that span several time scales the longest time scale, where the goal is capacity planning, I will describe the work of Blenstock, Raskina, Sanie Wang that considers ...

Also published in:

June 2005 SIGMETRICS Performance Evaluation Review Volume 33 Issue 1

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